



**CENTER FOR CONTINUING EDUCATION IN
ASSOCIATION WITH IEEE**



ORGANISES
ONE WEEK
ONLINE
FACULTY DEVELOPMENT PROGRAM
ON

**“Research Issues and Challenges in
Communication and Signal Processing-2024”**

25-30, March-2024

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING



SRI RAMAKRISHNA ENGINEERING COLLEGE

[Educational Service: SNR Sons Charitable Trust]

[Autonomous Institution, Re-accredited by NAAC with 'A+' Grade]

[Approved by AICTE and Permanently Affiliated to Anna University, Chennai]

[ISO 9001: 2015 Certified and all eligible programmes Accredited by NBA]

VATTAMALAIPALAYAM, N.G.G.O. COLONY POST, COIMBATORE – 641 022.



ABOUT THE INSTITUTION:

Sri Ramakrishna Engineering College (SREC), Coimbatore established in the year 1994 is one of the 17 institutions managed by SNR Sons Charitable Trust. The trust has a reputation of four and half decades in the field of education and medical services. SREC is an autonomous institution offering 12 Undergraduate programmes, 7 Postgraduate programmes in Engineering and Technology in addition to MBA. Sri Ramakrishna Engineering College is Re-accredited by NAAC with 'A+' grade and Seven of the B.E/B.Tech programmes have been accredited and re-accredited by National Board of Accreditation, New Delhi, since 2003. SREC has been consistently emerging with a remarkable infrastructure and educational quality, with rising employment opportunities.

ABOUT THE DEPARTMENT:

The Department of Electronics and Communication Engineering was established in the year 1994 offering Undergraduate programme in Electronics and Communication Engineering and two postgraduate programmes - VLSI Design and Embedded system Technologies.

The UG programme is re-accredited by National Board of Accreditation, New Delhi, since 2007. The department enables the students to attain excellence in domain technologies of Electronics and Communication with curricula focusing on the requirements specified by the industries. The interaction of the department with industries has laid path in delivering consultancy projects. The department is recognized as the research centre with all computing facilities, State-of-Art laboratories with 29 research scholars working towards potential solutions for the real-time problems. The department has 36 faculty members with 14 members holding doctoral degree, with competent expertise in the fields of VLSI Design, Embedded Systems, Image and Signal Processing, Communication and Networks, RF and MEMS. The department has received grants from AICTE, New Delhi, under various schemes specifically RPS, MODROBs, FDPs and Seminar Grants to the tune of Rs.1 Crore. The department has also received grants from DRDO, ISRO, BRNS ICMR, ISTE and IEEE for seminars and workshops.

WHO SHOULD ATTEND?

This Online Faculty development Programme is open for faculty from AICTE approved Institutions and research scholars from various Disciplines of engineering, who are interested in keeping themselves abreast with recent trends in Communication and Signal Processing.

ABOUT FDP:

Faculty Development Program (FDP) is a skill enhancement program for academic educators. It aims to upgrade their knowledge and skills in teaching, research, and management. FDP covers various topics such as case method, research methodology, area specialization, etc. FDP helps faculty to improve their performance and career prospects.

PROGRAMME CONTENTS:

At the end of the Online faculty development programme the participant will be able to :

- Describe the fundamentals and Road map of 5G/6G technology.
- Demonstrate to implement 5G/6G research ideas apply MATLAB Communication tool box.
- Explain antenna theory fundamentals for 5G/6G antenna design.

- *Explore AI/ML concepts in wireless communication research.
- *Apply VLSI design techniques for Wireless communication and more.
- *Other Areas that include Error control codes, Deep learning, RFMEMS, MANET/VANET, Quantum Computing and Optical Communication

ABOUT RESOURCE PERSONS:

Eminent professors from IITs, NITs, Industries and other resource persons from eminent institutions for this One Week FDP.

REGISTRATION FEE DETAILS:

Kindly remit the applicable registration fees and save the screen shot with your name and transaction id:

- IEEE Member : 350 /-
- Non –IEEE Member : 500 /-
- Industry Member : 750/-
(Inclusive GST)

After paying the fee, the candidate shall fill out the registration form and upload the payment receipt using this Google link given below. Contact the workshop coordinators for details regarding group registration. The deadline for registration is 22 March 2024.

REGISTRATION PROCESS:

Account name : Anupriya.V
Account No : 500101012798018
Bank Name : City Union Bank
IFSC code : CIUB00000034
Branch : Ram Nagar, coimbatore
UPI ID : apriya98@okaxis
G-Pay no : 7598237342

Scan QR code for Payment:



QR code for Registration link:



CONVENER:

Dr.M.Jagadeeswari,Head Of The Department/ECE

PROGRAMME CO-ORDINATORS:

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