

Hands on Training  
on

“Full Custom Design Flow using Cadence  
EDA Tool”

10<sup>th</sup> February 2023

## **REGISTRATION FORM**

Name : \_\_\_\_\_

Designation : \_\_\_\_\_

Organisation : \_\_\_\_\_

Address : \_\_\_\_\_

\_\_\_\_\_

Mobile : \_\_\_\_\_

E-mail : \_\_\_\_\_

Payment Details

Amount : \_\_\_\_\_

Accommodation: Yes/No

Place : \_\_\_\_\_

Date : \_\_\_\_\_

Signature of the Participant

## **ORGANISING COMMITTEE**

### **CHIEF PATRON**

Thiru.D.LAKSHMINARAYANASWAMY  
Managing Trustee

### **PATRON**

Thiru.R.SUNDAR  
Joint Managing Trustee

### **PRESIDENT**

Dr.N.R.ALAMELU  
Principal

### **CONVENOR**

Dr.M.JAGADEESWARI  
Professor & Head/ECE

### **COORDINATOR(S)**

Dr.C.S.MANIKANDABABU, Asso.Prof/ECE  
Dr.S.SHARMILA DEVI, AP(Sr.G)/ECE  
Dr.S.PADMAPRIYA, AP/ECE

### **ADDRESS FOR COMMUNICATION**

The Coordinator(s), Department of ECE,  
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**SRI RAMAKRISHNA ENGINEERING COLLEGE**

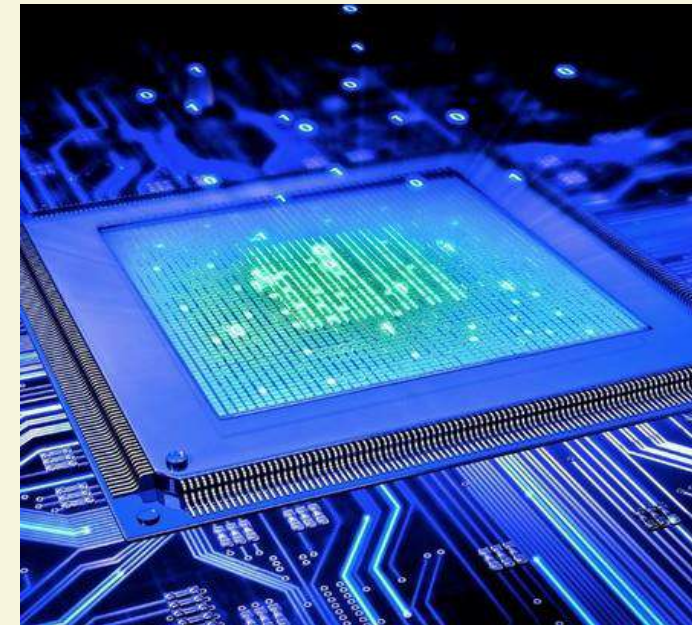
[Educational Service: SNR Sons Charitable Trust]  
[Autonomous Institution, Reaccredited by NAAC with 'A+' Grade]  
[Approved by AICTE and Permanently Affiliated to Anna University, Chennai]  
[ISO 9001:2015 Certified and all eligible programmes Accredited by NBA]  
Vattamalaipalayam, N.G.G.O. Colony Post, Coimbatore – 641 022.



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Organised By



**DEPARTMENT OF  
ELECTRONICS AND COMMUNICATION  
ENGINEERING IN ASSOCIATION WITH IETE**

# ABOUT THE INSTITUTION

Sri Ramakrishna Engineering College, Coimbatore established in the year 1994 by SNR Sons Charitable Trust is one of the 17 institutions managed by the trust. SREC is an autonomous institution, accredited by NAAC with ‘A+’ Grade offering 11 Undergraduate programmes and 09 Post Graduate programmes in Engineering and Technology including MBA. Seven of the B.E/B.Tech programmes have been accredited and re-accredited by multiple cycles of National Board of Accreditation, New Delhi, since 2003. A very good research culture exists in the campus with an average annual funding of Rs.4.5 crores from various funding agencies – AICTE, DST, CSIR, DRDO etc. The institution is also funded through DST-FIST, DST INSPIRE, DST-NSTMIS and Technology development schemes. The departments are closely working with industries – L&T, Bi-Meta Bearings, Roots Industries, Pricol Technologies, GE Healthcare, IAMPL, Bootlabs, Rolls-Royce, Cameron Manufacturing India Pt. Ltd., and other companies on consultancy, management development programmes a collaborative projects. The areas of collaboration include Machine vision systems, Finite element analysis, 3D printing, and Computational Fluid dynamics, Predictive Analytics, Virtual Instrumentation, Embedded Systems and IoT.

# ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering was established in the year 1994 offering Under Graduate programme in ECE and two Post Graduate Programmes - VLSI Design and Embedded System Technologies. The UG programme is accredited and re-accredited by multiple cycles of National Board of Accreditation, New Delhi, since 2003.

The department enable the students to attain excellence in the domain technologies of Electronics and Communication with curricula focusing on the requirements specified by the industries. The department has faculty members with competent expertise in the fields of VLSI Design, Embedded Systems, Image and Signal processing, Communication and Networks, RF and MEMS. The Department is recognized as a research Centre with all computing facilities State-of-the-art Laboratories with 33 research scholars working towards potential solutions for the real-time problems.

The department has received grants from AICTE under various schemes specifically RPS, MODROBs, FDPs and Seminar Grants. The department has also received grants from DRDO, ISRO, BRNS, ICMR, ISTE and IEEE towards seminars and workshops. The interaction of the department with industries has laid path in delivering consultancy projects. The department has exclusive alumni support for motivating and mentoring the students and they also facilitate the department through guest lectures, curriculum development, internships and placement activities.

# ABOUT THE WORKSHOP

The aim of this workshop is to provide hands - on experience on the state of- the art Cadence EDA tools for Digital IC Design. The participants will have an exposure to the VLSI Design flow – Overview Semicustom IC Design – Requirements and focus ASIC Design flow.

# RESOURCE PERSON

**Mr.Navaneetha Krishnan, Field Application Engineer, Entuple Technologies, Bengaluru.**

# COURSE CONTENTS

- ♦ ASIC Design Flow
- ♦ Synthesizable HDL Concepts
- ♦ Technology Library – An overview
- ♦ Automated RTL Synthesis using Cadence RTL
- ♦ Compiler
- ♦ Gate Level Netlist generation
- ♦ Physical design flow

# TARGET PARTICIPANTS

Students from UG, PG & Research Scholars

# REGISTRATION DETAILS

Registration fee: Rs 300/-

# REGISTER ONLINE

<https://forms.gle/YdUYHPAKYDwC3Vj4A>

# IMPORTANT DATES

Last date for receiving the filled Registration Form: **05/02/2023**

Notification of selected participants: **07/02/2023**